

APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

Title of Invention	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS
<p>Application Type : regular, utility Attorney Docket Number : BUR920030168US1</p>	
<p>Correspondence address: Customer Number: 23389</p> <div style="text-align: right; margin-top: -20px;"></div>	
<p>Inventors Information:</p> <p><u>Inventor 1:</u></p> <p>Applicant Authority Type: Inventor Citizenship: US Given Name: Darren Middle Name: L. Family Name: Anand Residence: City of Residence: Essex Junction State of Residence: VT Country of Residence: US Address-1 of Mailing Address: 53 Jericho Road Address-2 of Mailing Address: City of Mailing Address: Essex Junction State of Mailing Address: VT Postal Code of Mailing Address: 05452 Country of Mailing Address: US Phone: Fax: E-mail:</p> <p><u>Inventor 2:</u></p> <p>Applicant Authority Type: Inventor Citizenship: US Given Name: Kevin Middle Name: W.</p>	

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as our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

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